

Comparison of Cascaded H-bridge Inverter and Switched DC Source Inverter



^{#1}Bharati S.Mathapati, ^{#2}Prof. Shubhangi S.Landge

¹soni.mathapati@gmail.com
²shubhangi.landge1@gmail.com

^{#1}Student, Department of Electrical Engineering.
^{#2}Professor, Department of Electrical Engineering.

All India Shri Shivaji Memorial Society, Institute of Information Technology, Kennedy Road, Pune

ABSTRACT

A multilevel voltage-source inverters are the best solution for high power dc-to-ac conversion applications. A multilevel inverter is a linkage structure of multiple input dc levels and power semiconductor devices to get a quasisquare waveform. In addition, the multilevel waveform has a less harmonic contents as compared to a two-level waveform obtained from conventional inverters. The quality of the multilevel waveform is improved by increasing the number of levels. But ,If number of level increases, a large number of power semiconductor devices and gate driver circuits are also increased. So, it will increases system complexity and cost and tends to reduce the system reliability and efficiency. For a high-resolution waveform, therefore, practical considerations necessitate reduction in the number of switches and gate driver circuits. In this paper, the working principle of single phase five level switched DC sources inverter and single phase five level cascaded H-bridge inverter are explained. Performances of single phase five level switched DC sources inverter and single phase five level cascaded H-bridge inverter are compared on the basis of structure, working principle, number of power switches required and switching losses.

Keywords: Classical topologies, multilevel inverter (MLI), reduced component count, reduced switching losses.

ARTICLE INFO

Article History

Received: 20th June 2016

Received in revised form :
20th June 2016

Accepted: 23rd June 2016

Published online :

23rd June 2016

I. INTRODUCTION

Multilevel inverter technology has recently as a very important alternative in the area of high-power medium voltage applications. Researchers are going on to improve their capabilities further through optimized control techniques, and to minimize both component count and manufacturing cost. The multilevel inverter has been implemented in various applications, such as motor drives, power conditioning devices, renewable energy generation and distribution. PWM inverters can simultaneously control output voltage, frequency and it can reduce the amount of harmonics in output current which results in better THD content.

Several multilevel topologies have been developed, but increase in number of levels, it also increases the number of switches, number of independent dc sources, switching stresses, losses, voltage unbalancing across capacitors etc. Performances of switched DC sources inverter and cascaded H-bridge inverter are compared on the basis of

structure, working principle, number of switches used and switching losses.

A multilevel inverter consists multiple input DC sources and/or capacitors and power semiconductor devices to produce a quasi square waveform. Quality of output voltage waveform of a MLI indicates how much close in shape the waveform to the required sine wave. Quality of output voltage waveform of a MLI can be improved by increasing the number of levels. However, it results in a large number of power semiconductor devices and gate driver circuits. Therefore, system complexity and cost will be increased. Also, system reliability and efficiency will be reduced. Therefore, for multilevel inverters having higher number of levels in output voltage waveform, practical considerations necessitate reduction in the number of switches and gate driver circuits.

The paper is organized as follows. Section II presents structure and modes of operations of Cascaded H-bridge Inverter. Section III presents structure and modes of operations of switched dc source Inverter. Section IV

presents comparison in between cascaded H-bridge inverter and switched dc source inverter. In Section V conclusions are summarized.

II. STRUCTURE AND WORKING PRINCIPLE OF CASCADED H-BRIDGE INVERTER.

A single phase five level cascaded H-bridge inverter is shown in Fig. 2.1. A single phase five level cascaded H-bridge inverter consists two isolated input DC sources E_1 and E_2 . There are eight power switches(N-Channel MOSFETs) which are denoted by $S_1, S_2, S_3, S_4, S_5, S_6, S_7$ and S_8 . $v_{o1}(t), v_{o2}(t)$ and $v_o(t)$ denotes output voltages of H-bridge 1, H-bridge 2 and single phase five level cascaded H-bridge inverter respectively. Load current is indicated by $i_o(t)$.

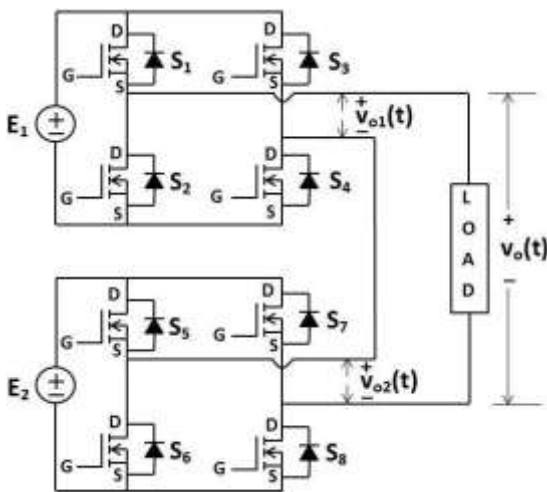


Fig. 2.1.: Single phase five level cascaded H-bridge inverter

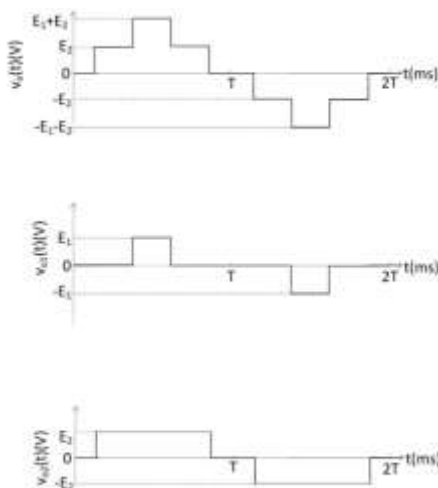


Fig. 2.2: Detailed output voltage waveform of single phase five level cascaded H-bridge inverter.

The detailed output voltage waveform of a single phase five level cascaded H-bridge inverter is shown in Fig. 2.2. The output voltage waveform consists of five levels $0, E_2, E_1 + E_2, -E_2, -E_1 - E_2$. Time period of output voltage is $2T$.

There are six modes of operations for single phase five level cascaded H-bridge inverter Modes of operation are explained below:

Mode 1:

In mode 1, switches S_1, S_3, S_5 and S_7 are turned on. Source is not connected to the load. So, Output voltage across load is zero.

Mode 2:

In mode 2, switches S_2, S_4, S_6 and S_8 are turned on. Source is not connected to the load. So, Output voltage across load is zero.

Mode 3:

In mode 3, switches S_1, S_3, S_5 and S_8 are turned on. So, Output voltage across load is E_2 .

Mode 4:

In mode 4 operation, switches S_1, S_4, S_5 and S_8 are turned on. So, Output voltage across load is $E_1 + E_2$.

Mode 5:

In mode 5, switches S_2, S_4, S_7 and S_6 are turned on. So, Output voltage across load is E_2 .

Mode 6:

In mode 6, switches S_3, S_2, S_7 and S_6 are turned on. So, Output voltage across load is $-E_1 - E_2$.

III. STRUCTURE AND WORKING PRINCIPLE OF SWITCHED DC SOURCE INVERTER

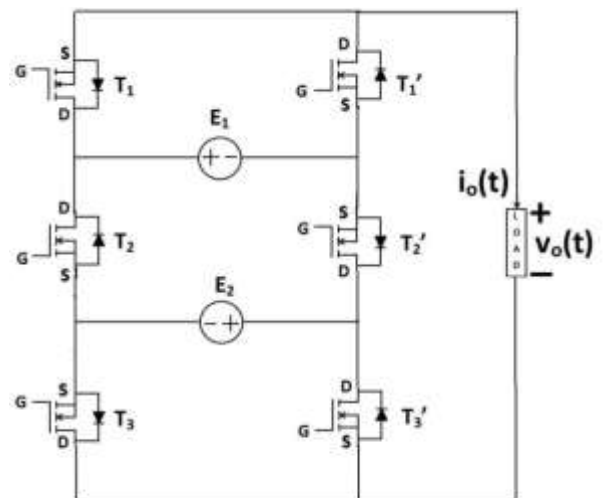


Fig. 3.1: Single phase five level switched DC sources inverter.

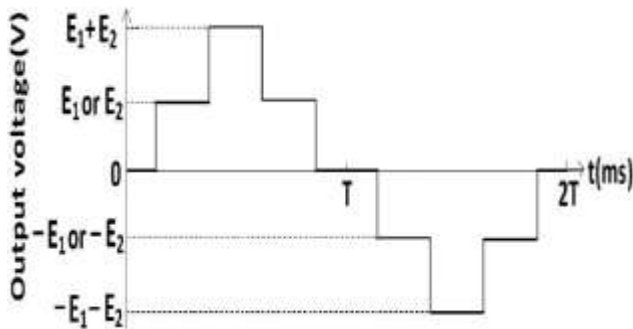


Fig. 3.2: Output voltage waveform of single phase five level switched DC sources inverter

A Single Phase Five Level Switched DC Sources Inverter is shown in Fig. 3.1. A Single Phase Five Level Switched DC Sources Inverter consist two isolated input DC sources E_1 and E_2 ($E_1=E_2$). There are three complementary pairs of power switches (N-Channel MOSFETs) which are denoted by (T_1, T_1') , (T_2, T_2') and (T_3, T_3') . The output voltage across load and load current are denoted by $v_o(t)$ and $i_o(t)$ respectively. The reference polarities of the output voltage $v_o(t)$ and reference direction of load current $i_o(t)$ are shown. The output voltage waveform of a single phase five level switched DC sources inverter when $E_1=E_2$ is shown in Fig. 3.2. The output voltage waveform consists of five levels 0, E_1 or E_2 , $E_1 + E_2$, $-E_1$ or $-E_2$, $-E_1-E_2$. Time period of output voltage is $2T$.

There are eight modes of operations for single phase five level switched DC source. Modes of operation are explained below:

Mode 1:

In mode 1, The switches T_1' , T_2' and T_3' are turned on. No source is connected to the load. So, Output voltage across load is zero.

Mode 2:

In mode 2, The switches T_1 , T_2 and T_3 are turned on. No source is connected to the load. So, Output voltage across load is zero.

Mode 3:

In mode 3, The switches T_1 , T_2' and T_3' are turned on. So, Output voltage across load is $+E_1$.

Mode 4:

In mode 4, The switches T_1' , T_2' and T_3 are turned on. So, Output voltage across load is $+E_2$.

Mode 5:

In mode 5, switches T_1 , T_2' and T_3 are turned on. So, Output voltage across load is $E_1 + E_2$.

Mode 6:

In mode 6, switches T_1' , T_2 and T_3 are turned on. So, Output voltage across load is $-E_1$.

Mode 7:

In mode 7, The switches T_1 , T_2 and T_3' are turned on. So, Output voltage across load is $-E_2$.

Mode 8:

In mode 8, switches T_1' , T_2 and T_3' are turned on. So, Output voltage across load is $-E_1-E_2$.

IV. COMPARISON OF SWITCHED DC SOURCE INVERTER WITH CASCADED H-BRIDGE INVERTER

A comparison between the switched dc source inverter and CHB inverter is carried out in terms of component requirements and switching losses.

a) Power Switch Requirements: If CHB Inverter consist n number of dc sources, then total $4n$ switches are required, while the dc switched source inverter requires " $2n + 2$ " switches. If the comparison is made for three-phase inverters, the difference in the numbers of switches required becomes significantly large. For example, In case of a three-phase 11-level voltage, a CHB inverter requires 60 switches and the dc switched source inverter needs only 36 switches. So, number of switches required for switched dc source inverter is less than CHB inverter with increase in number of levels.

b) Switching Losses: The dc switched source inverter has less switching losses as compared to cascaded H-bridge inverter. By considering the case of a five-level inverter with two equal input dc sources of voltage V_{dc} , the average switching power loss p_s in the switch caused by switching operations can be defined using as,

$$p_s = (1/6)V_o.I_o (t_{on} + t_{off}) \cdot f \quad (1)$$

where t_{on} and t_{off} are the turn-on and turn-off intervals respectively, V_o is the voltage blocked by the switch, I_o is the switch current, and f is the switching frequency. For simplification, it is assumed that switches operate with the same t_{on} and t_{off} values while they carry the same current I_o . Thus, p_s can be expressed as

$$p_s = \zeta V_o.f \quad (2)$$

where $\zeta [= (1/6)I_o(t_{on} + t_{off})]$ is a constant. Thus, switching power losses in the CHB five-level inverter in which all eight switches operate at high switching frequency f_s , while blocking the same voltage V_{dc} can be averaged as p_s ,

$$p_s, CHB/5L = 8\zeta V_{dc}.f_s \quad (3)$$

Similarly, the switching power loss of switched source five-level inverter with six switches can be obtained as p_s ,

$$p_s, SDCSI/5L = \zeta \{4V_{dc}.f_s + 2(2V_{dc}).f_o\} \quad (4)$$

This is so because four switches which block voltage V_{dc} operate at high switching frequency f_s and two switches

which block voltage $2V_{dc}$ operate at fundamental frequency f_0 . Hence,

$$p_s, SDCSI/5L = 4\zeta V_{dc}(f_s + f_0). \quad (5)$$

But, f_0 is too much small as compared to f_s , so the switching losses can be approximated as,

$$p_s, SDCSI/5L = 4\zeta V_{dc}.f_s. \quad (6)$$

From (3) and (6), It is clear that the switching losses in switched dc source inverter is half of the switching losses in CHB inverter.

V. CONCLUSION

In this paper performances of single phase five level switched DC source inverter is compared with single phase five level cascaded H bridge inverter on the basis of structure, working principle, number of power switches required and Switching losses. Single phase five level switched DC source inverter is more competitive than single phase five level cascaded H- bridge inverter .So, switched dc source inverter is can be effectively employed for applications where isolated dc sources are available.

REFERENCES

- [1] Krishna Kumar Gupta, and Shailendra Jain, "A novel multilevel inverter based on switched DC sources," IEEE Trans. on Ind. Electron., vol. 61, no. 7, pp. 3269-3278, July 2014.
- [2] M. Malinowski, K. Gopakumar, J. Rodriguez, and M. A. Pérez, "A survey on cascaded multilevel inverters," IEEE Trans. Ind. Electron., vol. 57, no. 7, pp. 2197–2206, Jul. 2010.
- [3] J. Ebrahimi, E. Babaei, and G. B. Gharehpetian, "A new multilevel converter topology with reduced number of power electronic components," IEEE Trans. Ind. Electron., vol. 59, no. 2, pp. 655–667, Feb. 2012.
- [4] G. Buticchi, E. Lorenzani, and G. Franceschini, "A five-level single-phase grid-connected converter for renewable distributed systems," IEEE Trans. Ind. Electron., vol. 60, no. 3, pp. 906–918, Mar. 2013.